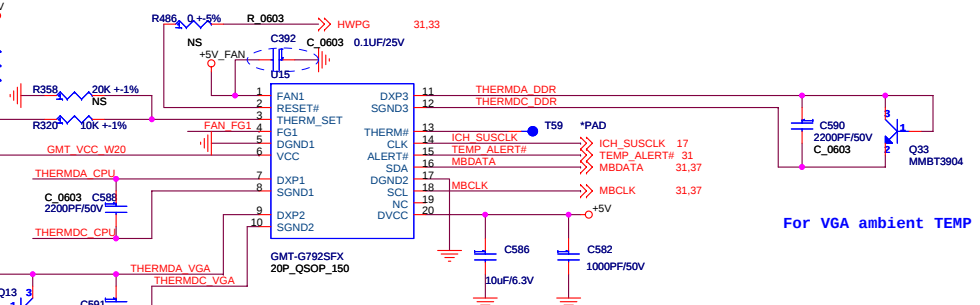
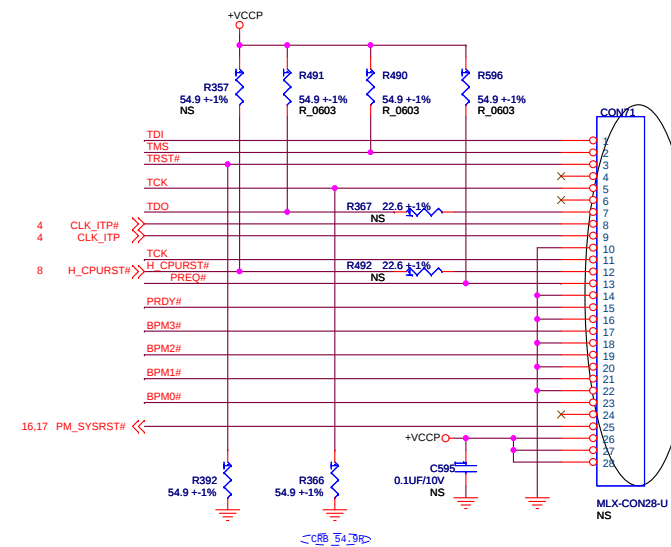




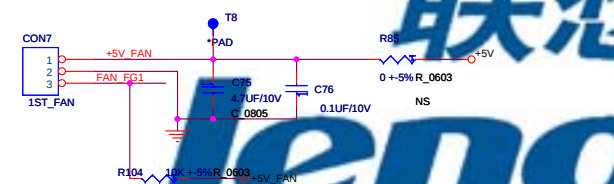
Check the CLK14_ICH6 clock SI after bring up!
If need changed to 33R





NOTE:

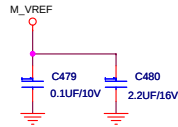
- 1.hardware shutdown for sensor2.
 $THERM_SET = [(Tset - 72) \times 0.02 + 0.34] \times VCC$
THERM_SET pin voltage from 0.35VCC to 0.95 VCC
and trigger point set from 72°C to 102°C, 2°C a step
- 2.software shutdown for sensor1, sensor3.



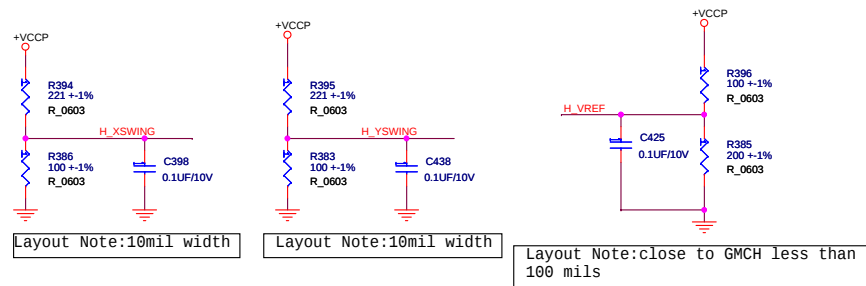
Layout note:
Comp0,2 connect with $Z_0=27.4\Omega$, make trace length shorter than $0.5''$.
Comp1,3 connect with $Z_0=55\Omega$, make trace length shorter than $0.5''$.

For VGA ambient TEMP

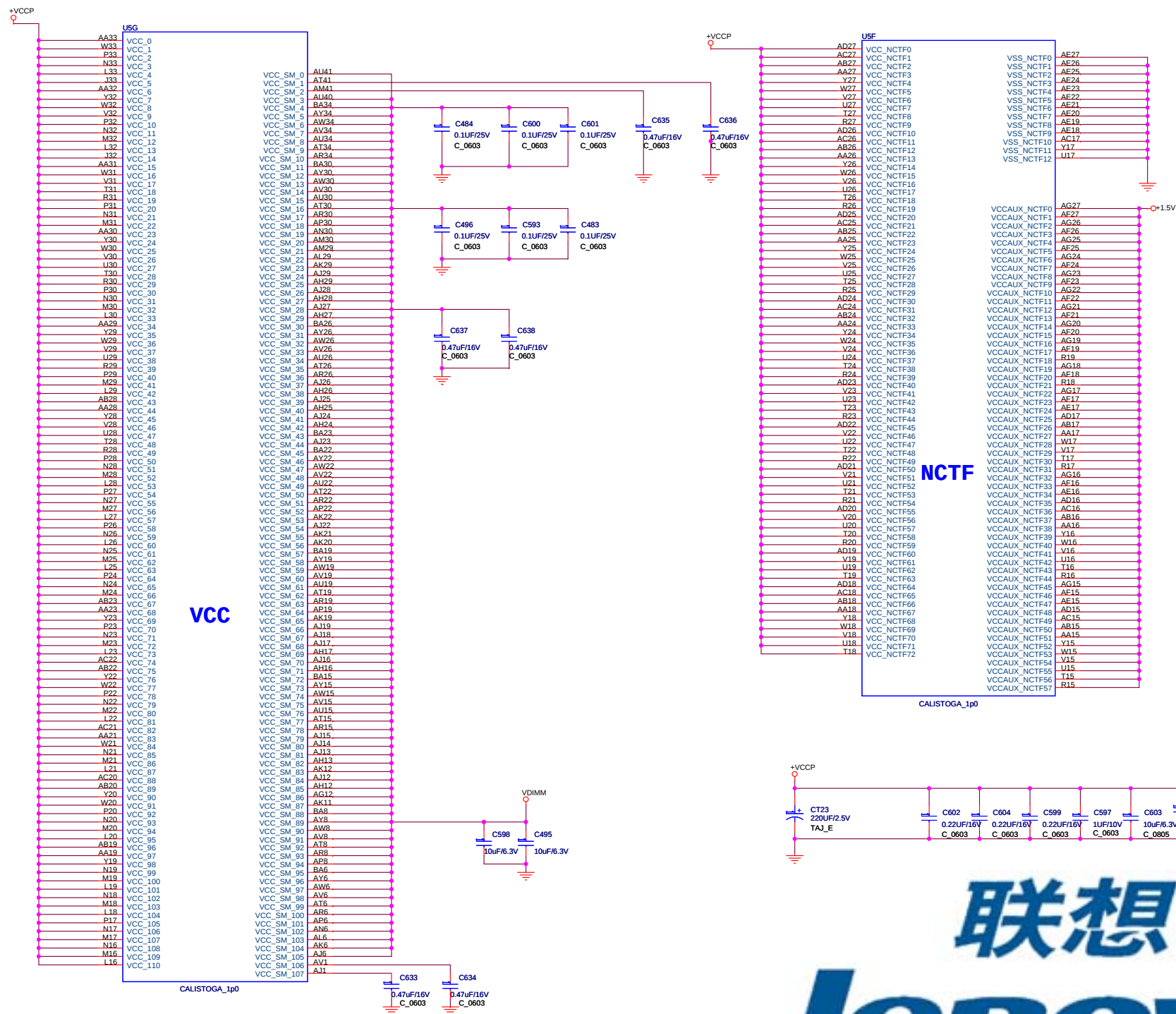




CFG[17:3] INTERNAL PULLUP
CFG[20:18] INTERNAL
PULLDOWN







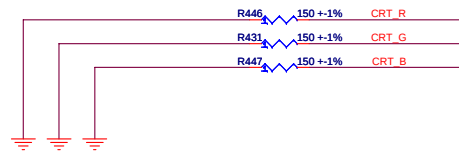
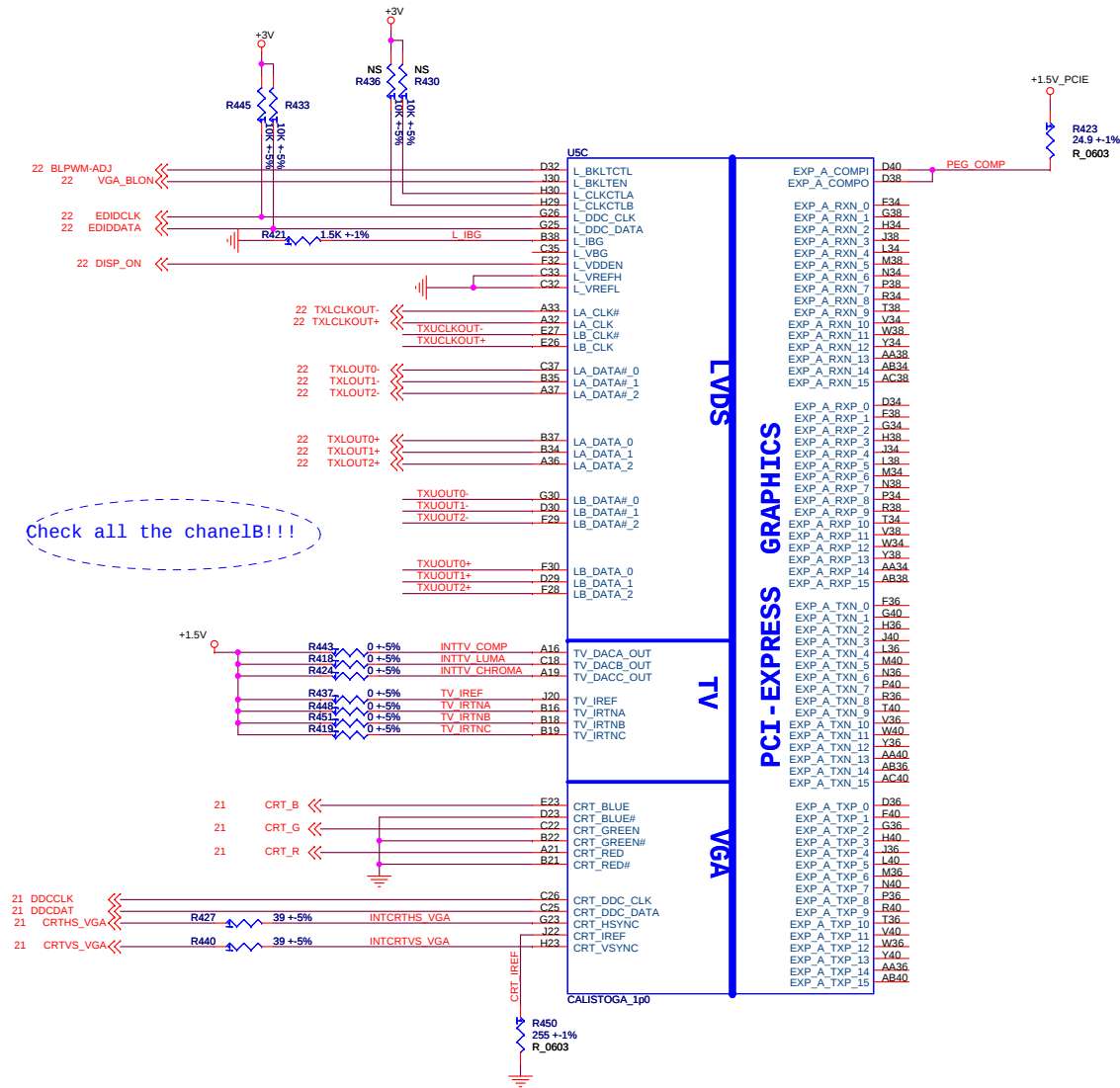


AE12	VCCAUX38
AD12	VCCAUX39
	VCCAUX40

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USI		
AC41	VSS_0	VSS_97
AA41	VSS_1	VSS_98
WA1	VSS_2	VSS_99
T41	VSS_3	VSS_100
F41	VSS_4	VSS_101
M41	VSS_5	VSS_102
F41	VSS_6	VSS_103
AV40	VSS_7	VSS_104
AP40	VSS_8	VSS_105
AM40	VSS_9	VSS_106
AK40	VSS_10	VSS_107
A40	VSS_11	VSS_108
AE40	VSS_12	VSS_109
AG40	VSS_13	VSS_110
AF40	VSS_14	VSS_111
AE40	VSS_15	VSS_112
B40	VSS_16	VSS_113
AV39	VSS_17	VSS_114
AW39	VSS_18	VSS_115
AV39	VSS_19	VSS_116
AV39	VSS_20	VSS_117
AR39	VSS_21	VSS_118
AN39	VSS_22	VSS_119
A139	VSS_23	VSS_120
AC39	VSS_24	VSS_121
AB39	VSS_25	VSS_122
AA39	VSS_26	VSS_123
V39	VSS_27	VSS_124
W39	VSS_28	VSS_125
V39	VSS_29	VSS_126
T39	VSS_30	VSS_127
R39	VSS_31	VSS_128
P39	VSS_32	VSS_129
N39	VSS_33	VSS_130
M39	VSS_34	VSS_131
L39	VSS_35	VSS_132
L39	VSS_36	VSS_133
H39	VSS_37	VSS_134
G39	VSS_38	VSS_135
F39	VSS_39	VSS_136
D39	VSS_40	VSS_137
AT38	VSS_41	VSS_138
AM38	VSS_42	VSS_139
AH38	VSS_43	VSS_140
AG38	VSS_44	VSS_141
AF38	VSS_45	VSS_142
AE38	VSS_46	VSS_143
C38	VSS_47	VSS_144
AK37	VSS_48	VSS_145
AH37	VSS_49	VSS_146
AB37	VSS_50	VSS_147
AA37	VSS_51	VSS_148
L37	VSS_52	VSS_149
W37	VSS_53	VSS_150
V37	VSS_54	VSS_151
T37	VSS_55	VSS_152
R37	VSS_56	VSS_153
P37	VSS_57	VSS_154
N37	VSS_58	VSS_155
M37	VSS_59	VSS_156
L37	VSS_60	VSS_157
L37	VSS_61	VSS_158
H37	VSS_62	VSS_159
G37	VSS_63	VSS_160
F37	VSS_64	VSS_161
D37	VSS_65	VSS_162
AV36	VSS_66	VSS_163
AW36	VSS_67	VSS_164
AN36	VSS_68	VSS_165
AH36	VSS_69	VSS_166
AG36	VSS_70	VSS_167
AF36	VSS_71	VSS_168
AE36	VSS_72	VSS_169
AC36	VSS_73	VSS_170
C36	VSS_74	VSS_171
B36	VSS_75	VSS_172
BA35	VSS_76	VSS_173
AV35	VSS_77	VSS_174
AR35	VSS_78	VSS_175
AH35	VSS_79	VSS_176
AB35	VSS_80	VSS_177
AA35	VSS_81	VSS_178
V35	VSS_82	VSS_179
W35	VSS_83	
V35	VSS_84	
T35	VSS_85	
R35	VSS_86	
P35	VSS_87	
N35	VSS_88	
M35	VSS_89	
L35	VSS_90	
L35	VSS_91	
H35	VSS_92	
G35	VSS_93	
F35	VSS_94	
D35	VSS_95	
AN34	VSS_96	

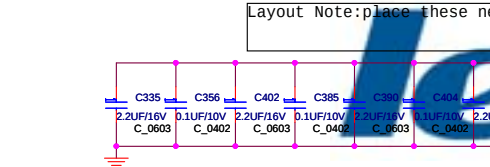
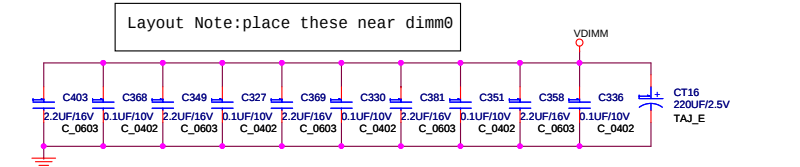
CALISTOGA_1p0

USJ		
AT23	VSS_180	VSS_273
AM23	VSS_181	VSS_274
AE23	VSS_182	VSS_275
AC23	VSS_183	VSS_276
AW23	VSS_184	VSS_277
AV23	VSS_185	VSS_278
K23	VSS_186	VSS_279
E23	VSS_187	VSS_280
C23	VSS_188	VSS_281
AA22	VSS_189	VSS_282
K22	VSS_190	VSS_283
G22	VSS_191	VSS_284
R22	VSS_192	VSS_285
E22	VSS_193	VSS_286
D22	VSS_194	VSS_287
A22	VSS_195	VSS_288
BA21	VSS_196	VSS_289
AV21	VSS_197	VSS_290
AR21	VSS_198	VSS_291
AH21	VSS_199	VSS_292
AL21	VSS_200	VSS_293
AE21	VSS_201	VSS_294
Y21	VSS_202	VSS_295
P21	VSS_203	VSS_296
K21	VSS_204	VSS_297
R21	VSS_205	VSS_298
C21	VSS_206	VSS_299
AW20	VSS_207	VSS_300
AM20	VSS_208	VSS_301
AE20	VSS_209	VSS_302
AV20	VSS_210	VSS_303
AK20	VSS_211	VSS_304
V20	VSS_212	VSS_305
A20	VSS_213	VSS_306
AC19	VSS_214	VSS_307
W19	VSS_215	VSS_308
K19	VSS_216	VSS_309
G19	VSS_217	VSS_310
N19	VSS_218	VSS_311
AH18	VSS_219	VSS_312
G18	VSS_220	VSS_313
K18	VSS_221	VSS_314
P18	VSS_222	VSS_315
E18	VSS_223	VSS_316
D18	VSS_224	VSS_317
A18	VSS_225	VSS_318
AV17	VSS_226	VSS_319
BA17	VSS_227	VSS_320
AP17	VSS_228	VSS_321
AM17	VSS_229	VSS_322
AK17	VSS_230	VSS_323
AV16	VSS_231	VSS_324
AN16	VSS_232	VSS_325
AD16	VSS_233	VSS_326
AL16	VSS_234	VSS_327
W16	VSS_235	VSS_328
J16	VSS_236	VSS_329
E16	VSS_237	VSS_330
AP15	VSS_238	VSS_331
AM15	VSS_239	VSS_332
AK15	VSS_240	VSS_333
J15	VSS_241	VSS_334
M15	VSS_242	VSS_335
L15	VSS_243	VSS_336
B15	VSS_244	VSS_337
BA14	VSS_245	VSS_338
M14	VSS_246	VSS_339
AT14	VSS_247	VSS_340
AK14	VSS_248	VSS_341
F14	VSS_249	VSS_342
AA14	VSS_250	VSS_343
U14	VSS_251	VSS_344
P14	VSS_252	VSS_345
K14	VSS_253	VSS_346
E14	VSS_254	VSS_347
D14	VSS_255	VSS_348
AN13	VSS_256	VSS_349
AM13	VSS_257	VSS_350
AL13	VSS_258	VSS_351
AG13	VSS_259	VSS_352
P13	VSS_260	VSS_353
F13	VSS_261	VSS_354
D13	VSS_262	VSS_355
B13	VSS_263	VSS_356
AY12	VSS_264	VSS_357
AC12	VSS_265	VSS_358
K12	VSS_266	VSS_359
H12	VSS_267	VSS_360
E12	VSS_268	
AD11	VSS_269	
AA11	VSS_270	
Y11	VSS_271	
	VSS_272	

CALISTOGA_1p0

VSS



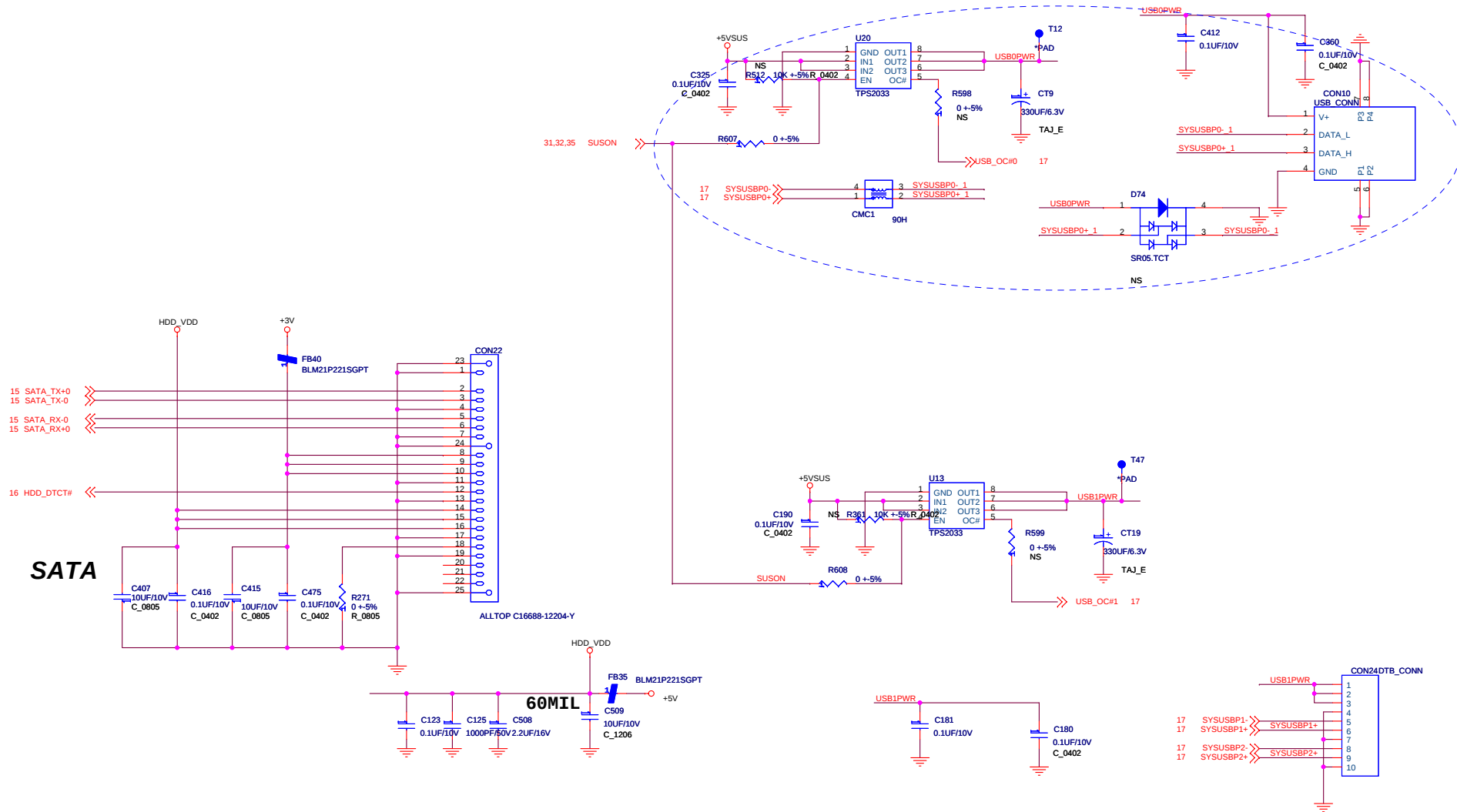


[illegible]

Layout note: R143 needs to be placed within 2" of ICH7, R158 must be placed within 2" of R143 w/o stub.

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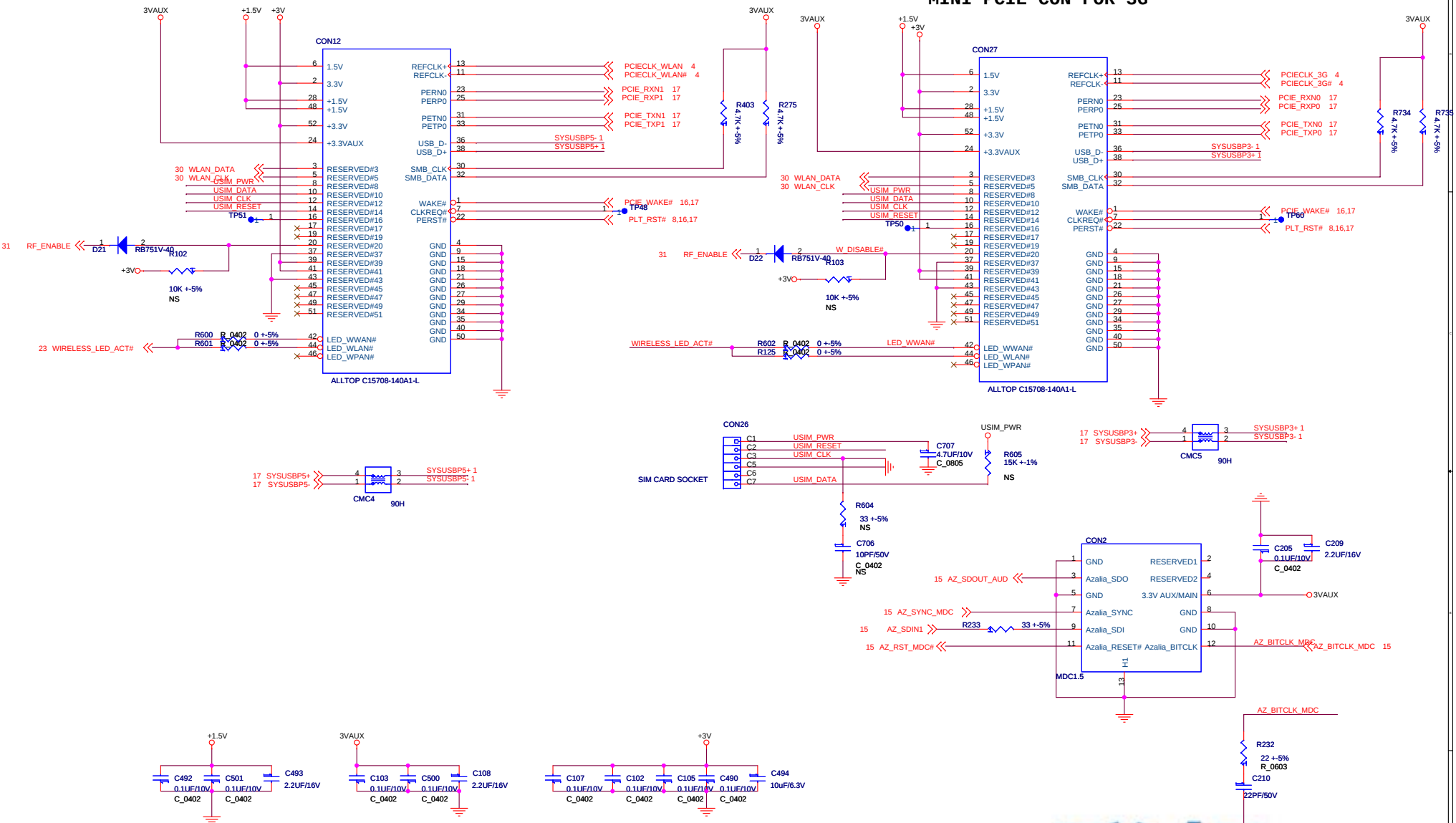




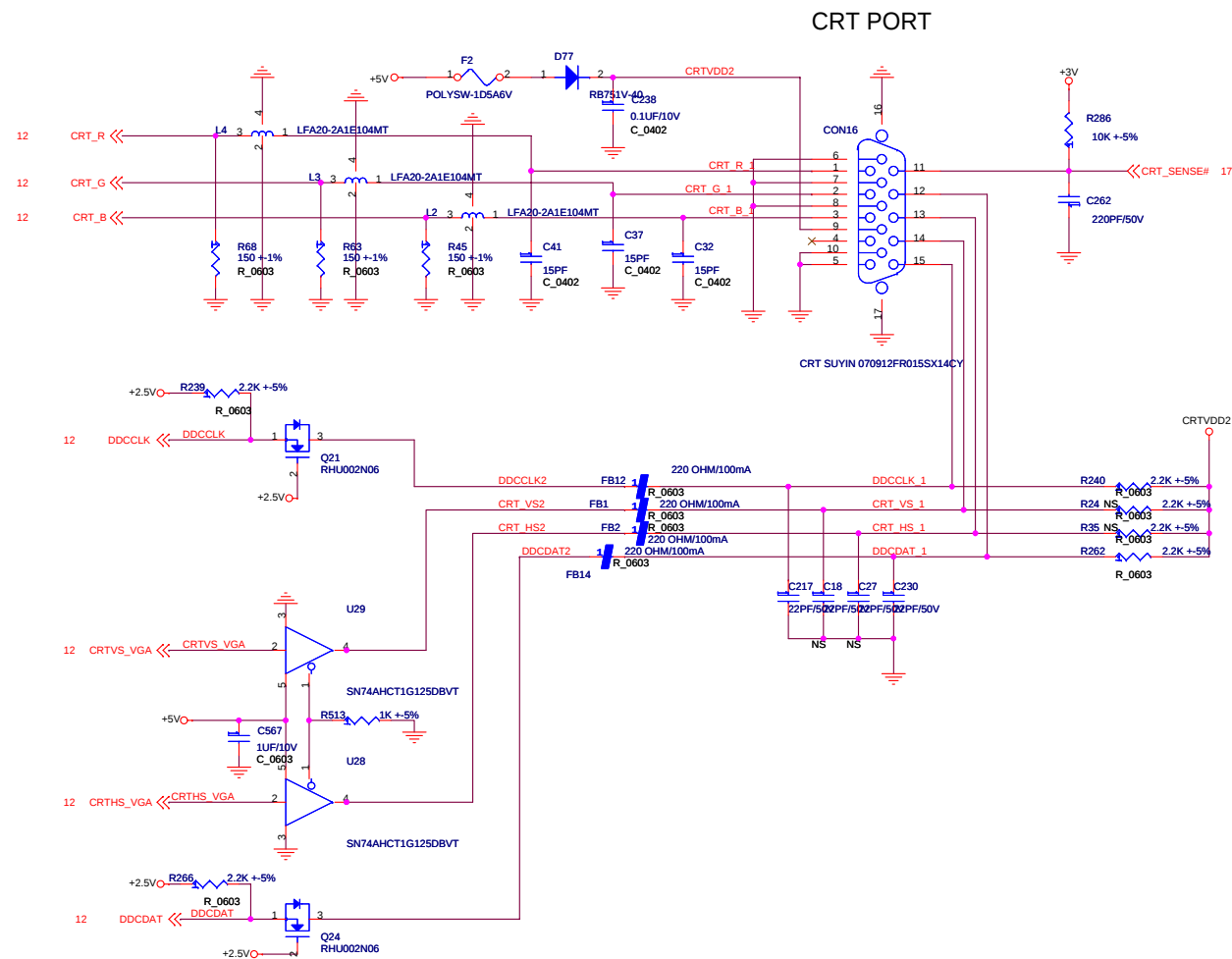
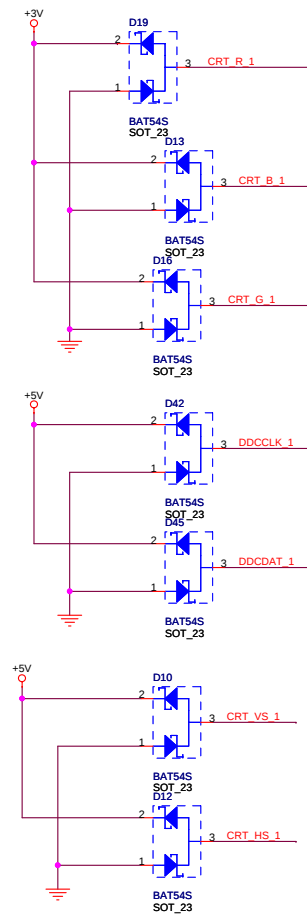
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MINI PCIE CON FOR WLAN

MINI PCIE CON FOR 3G

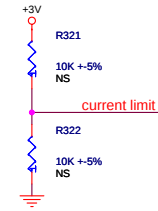
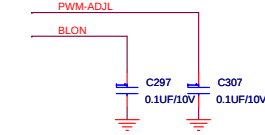


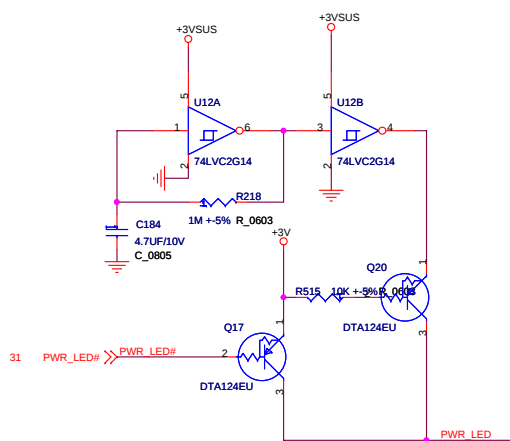
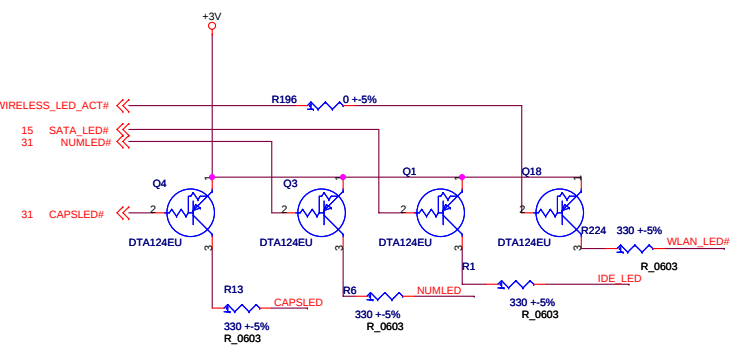
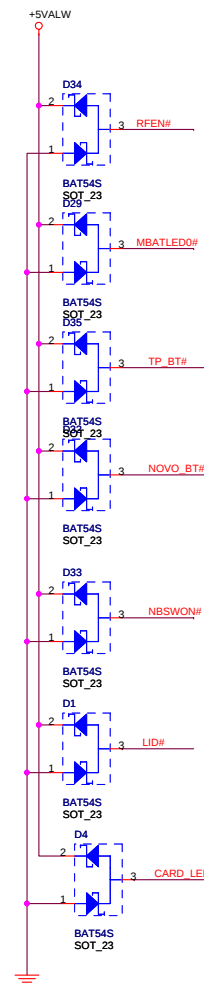
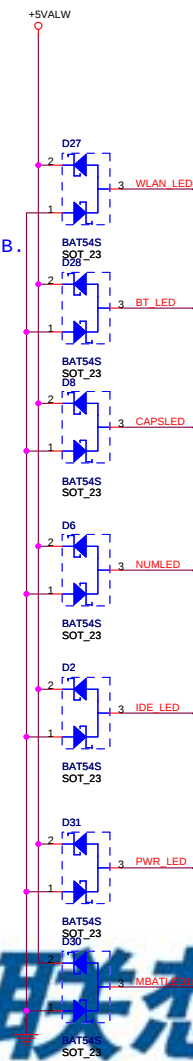
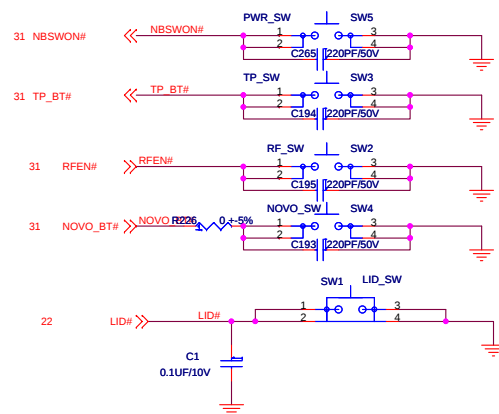
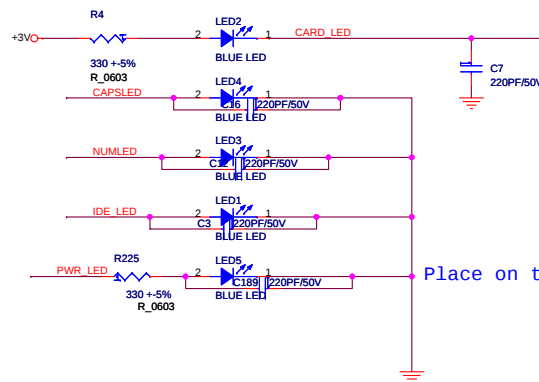
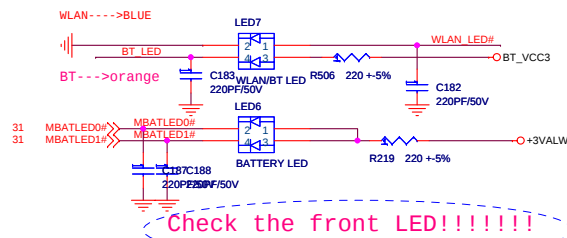
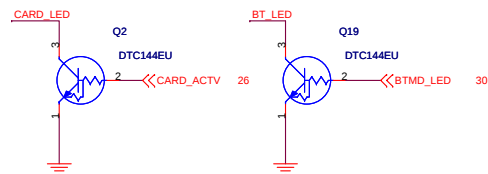
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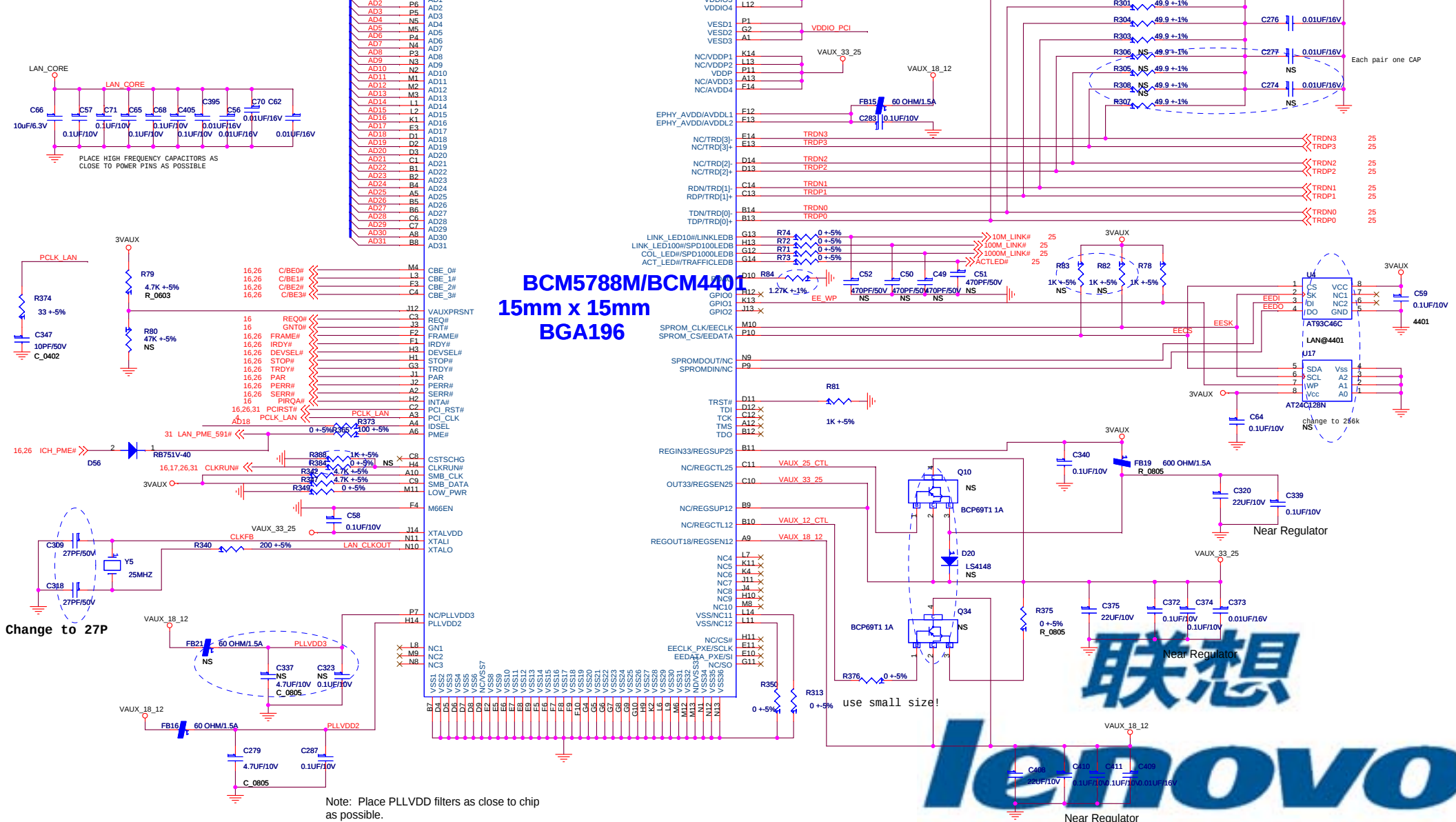
Check new spec and PIN1, 31





Component Stuffed Requirements

	4401	5788
R401	NS	S
R402	S	NS
R384	S	NS
R388	S	NS
FB21 C337 C323	NS	NS
R84	1.27K 1%	1.24K 1%
Q10 Q34	NS	S
R83 R82	NS	S
R305 R306	NS	S
R307 R308	NS	S
C277 C274	NS	S
U4	S	NS
U17	NS	S



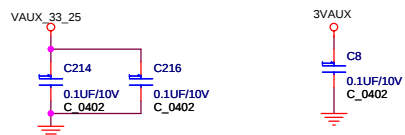
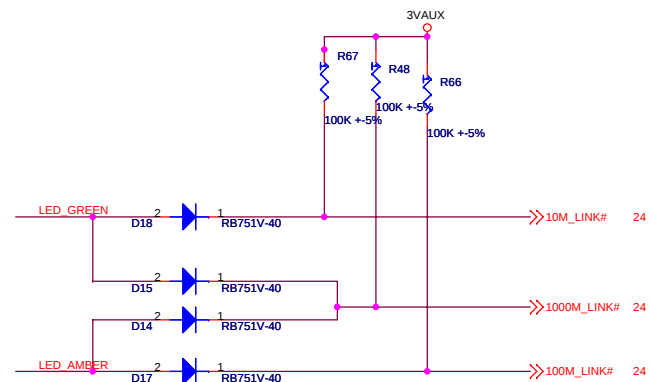
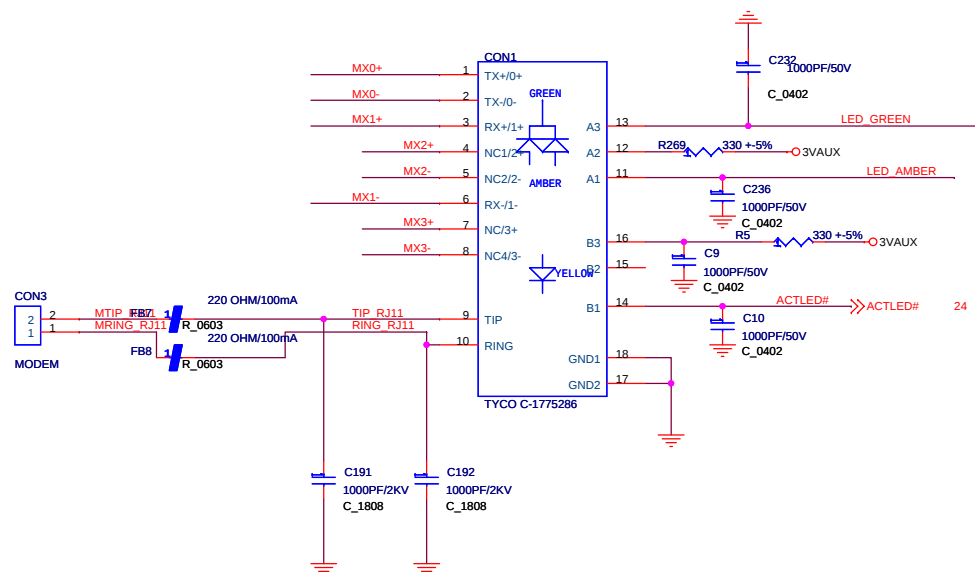
BCM5788M/BCM4401
15mm x 15mm
BGA196

Change to 27P

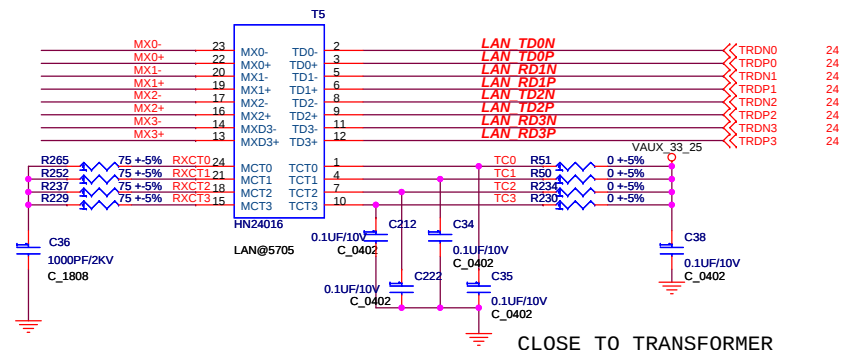
use small size!

Note: Place PLLVDD filters as close to chip as possible.



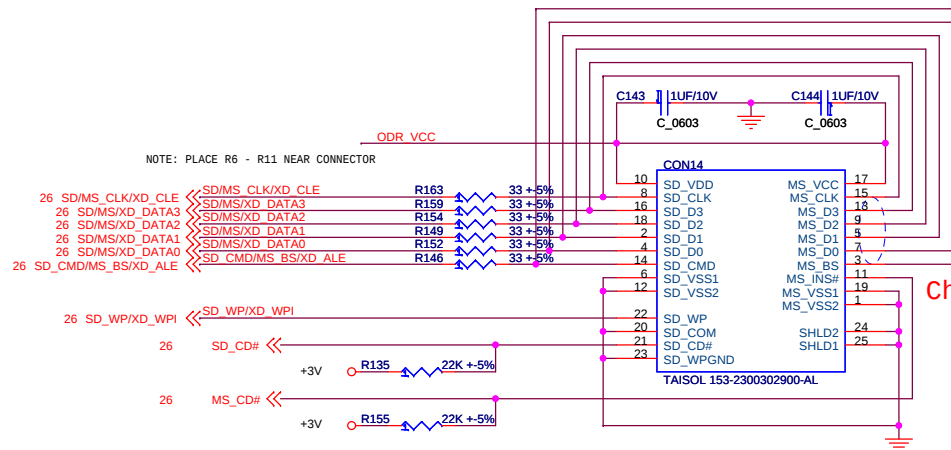


EMI PAD

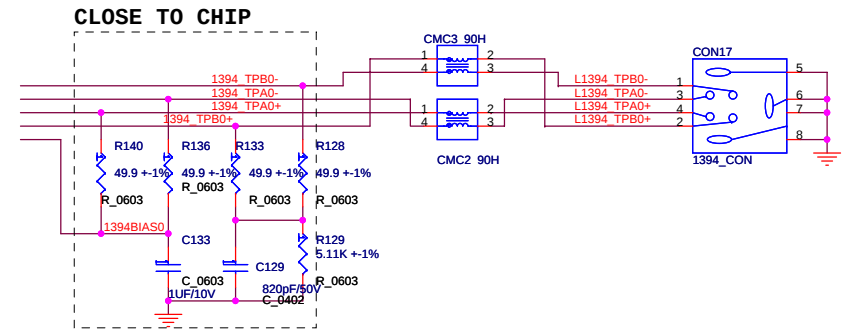
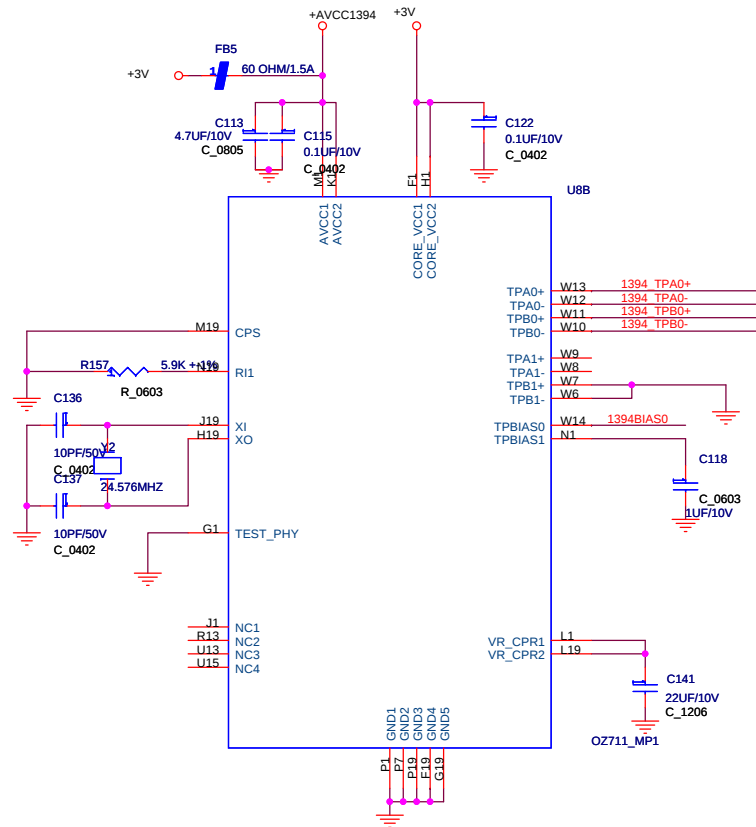
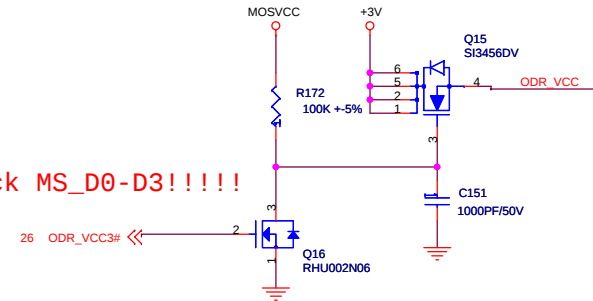


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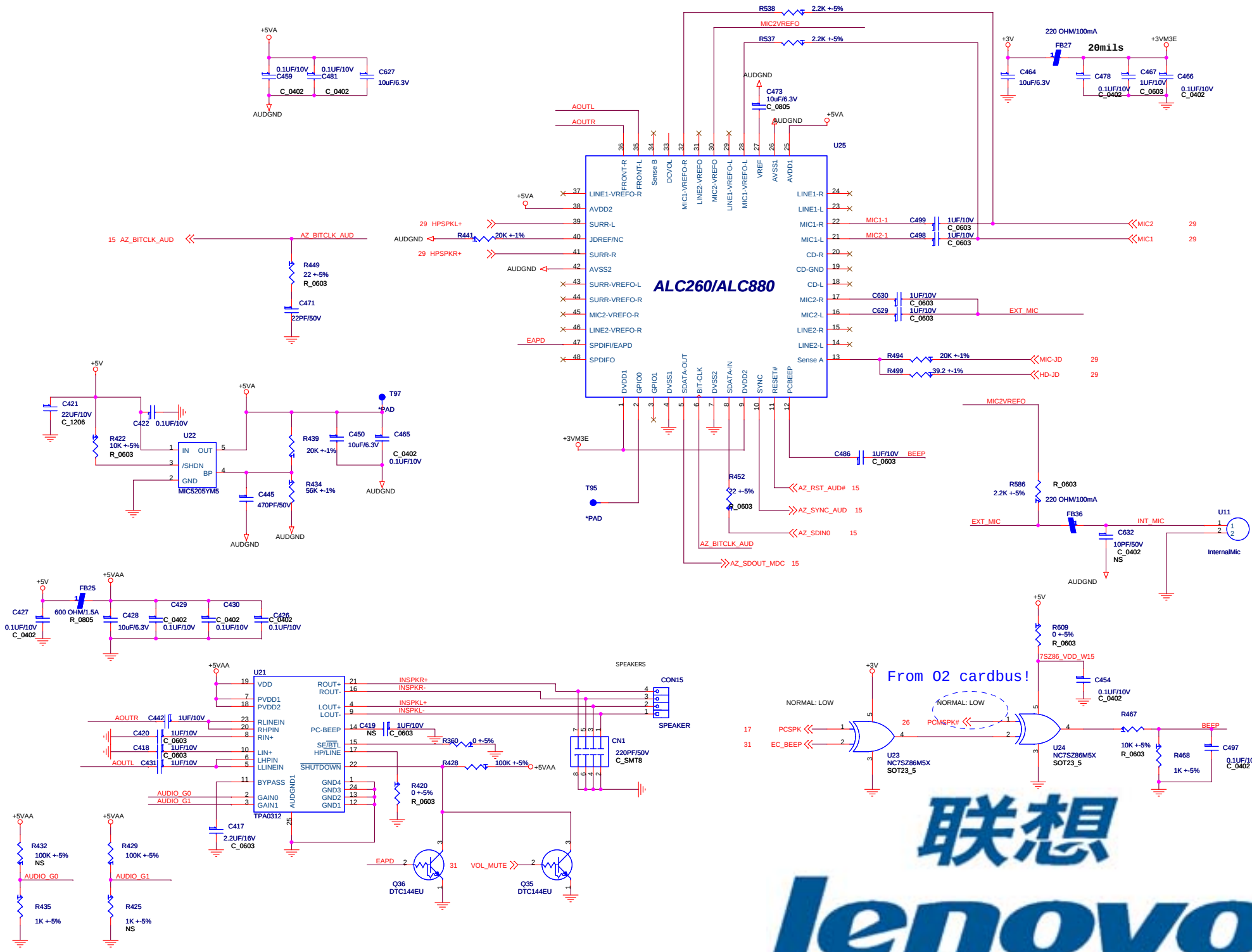


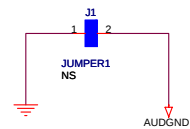


Check MS_D0-D3!!!!!!

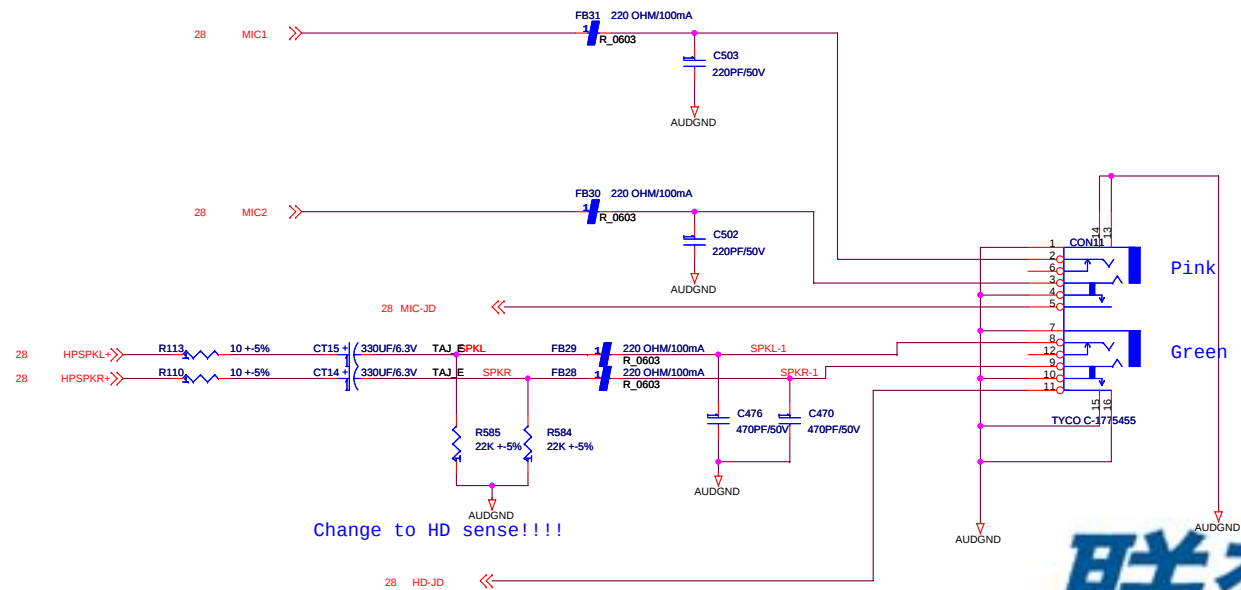


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J1 is only for Layout convenience. GND and AUDGND must be connected in inner layer.

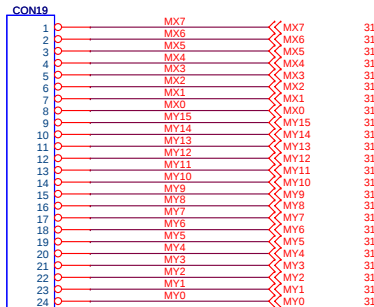


Change to HD sense!!!!

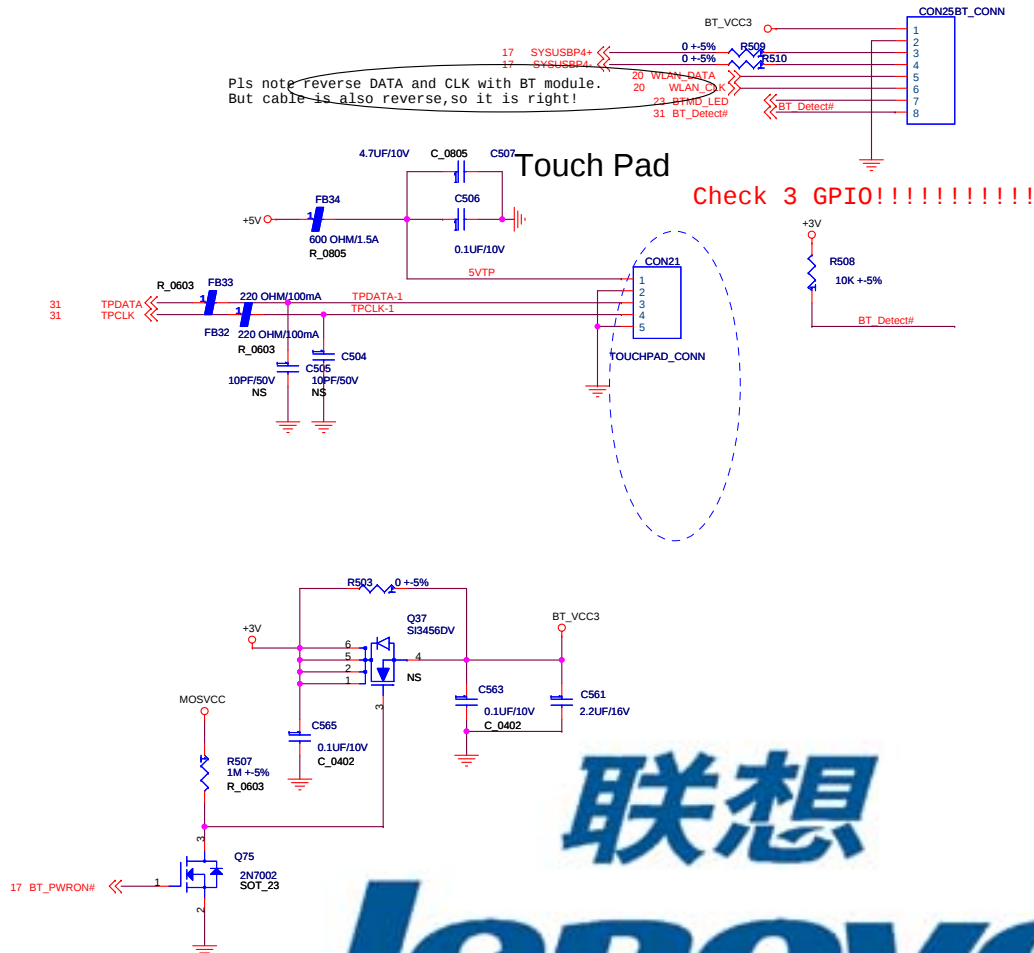
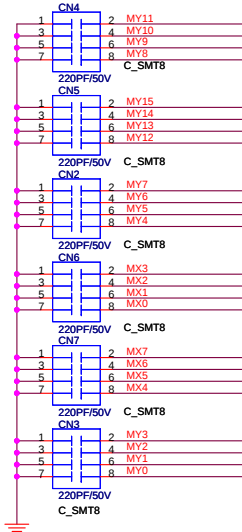
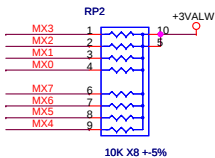
Consider to reserve PLUG signal in AC 97 platform



Check Keyboard matrix

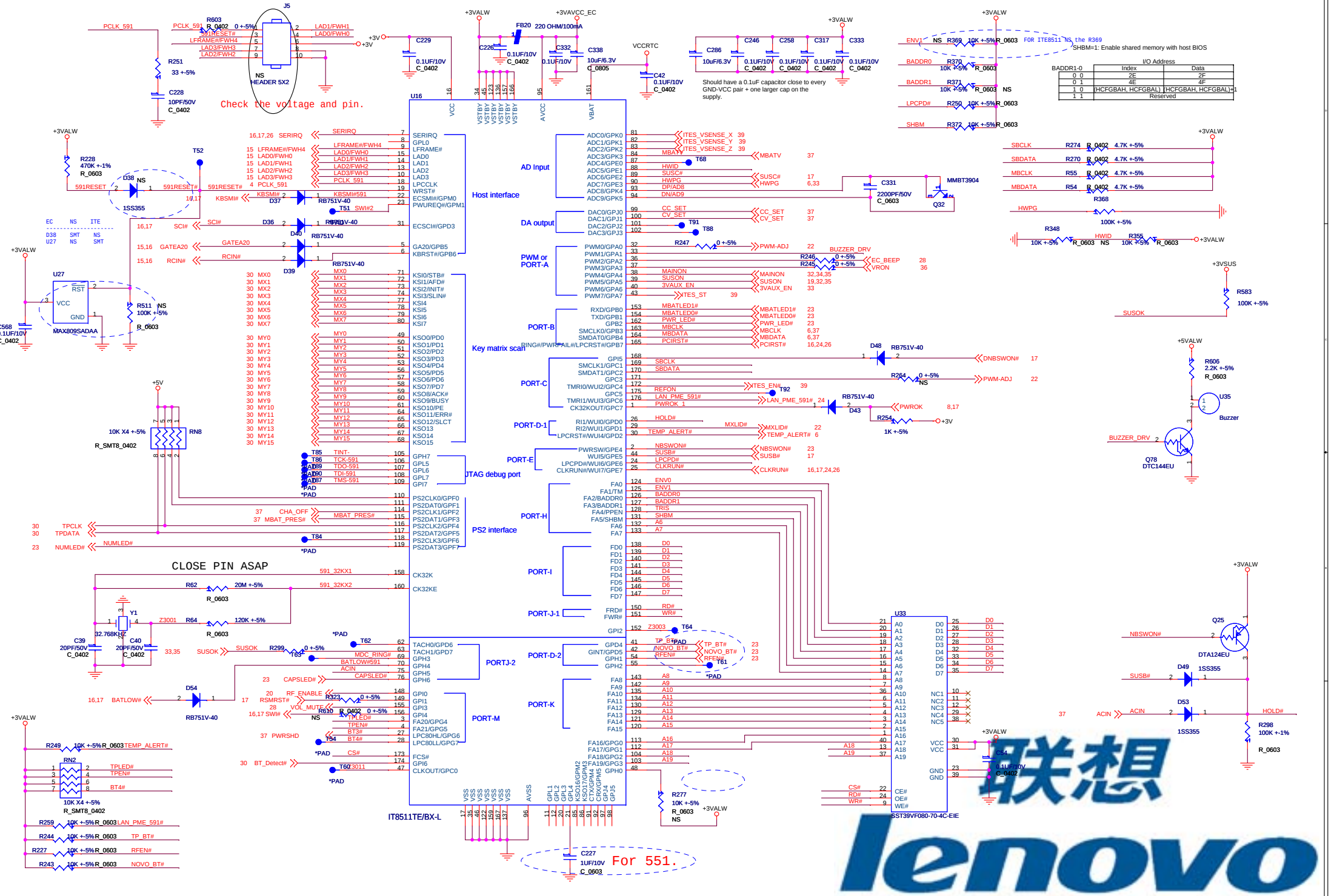


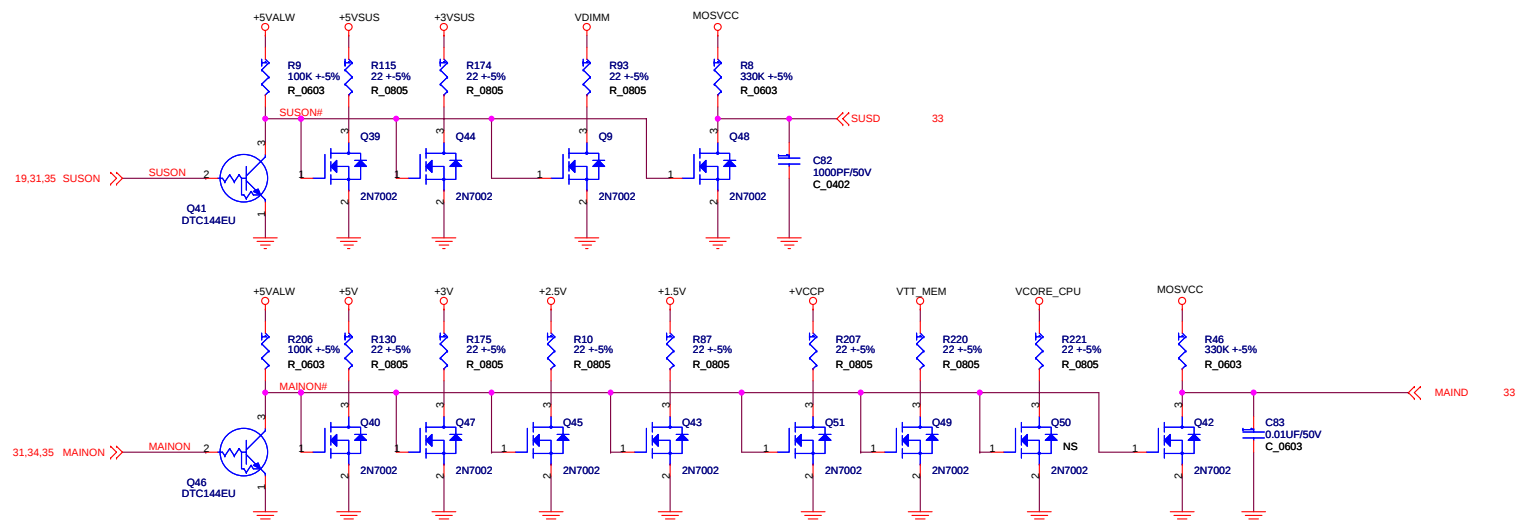
NEED CHECK STICK POINTER PIN DEFINE



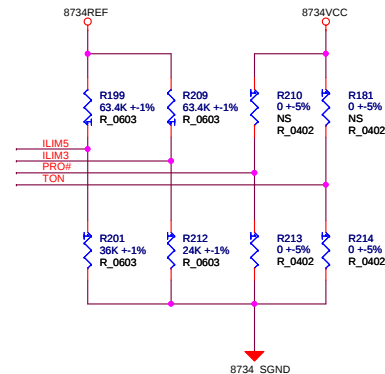
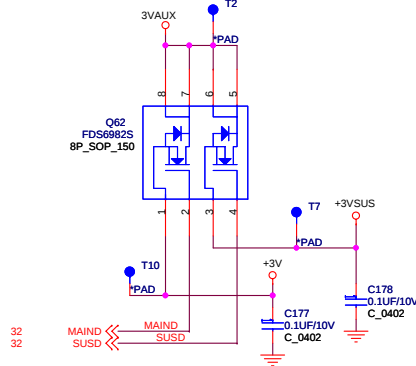
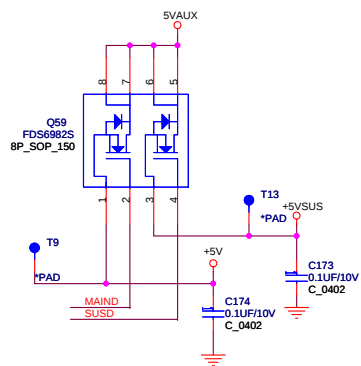
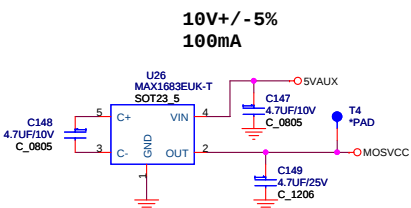
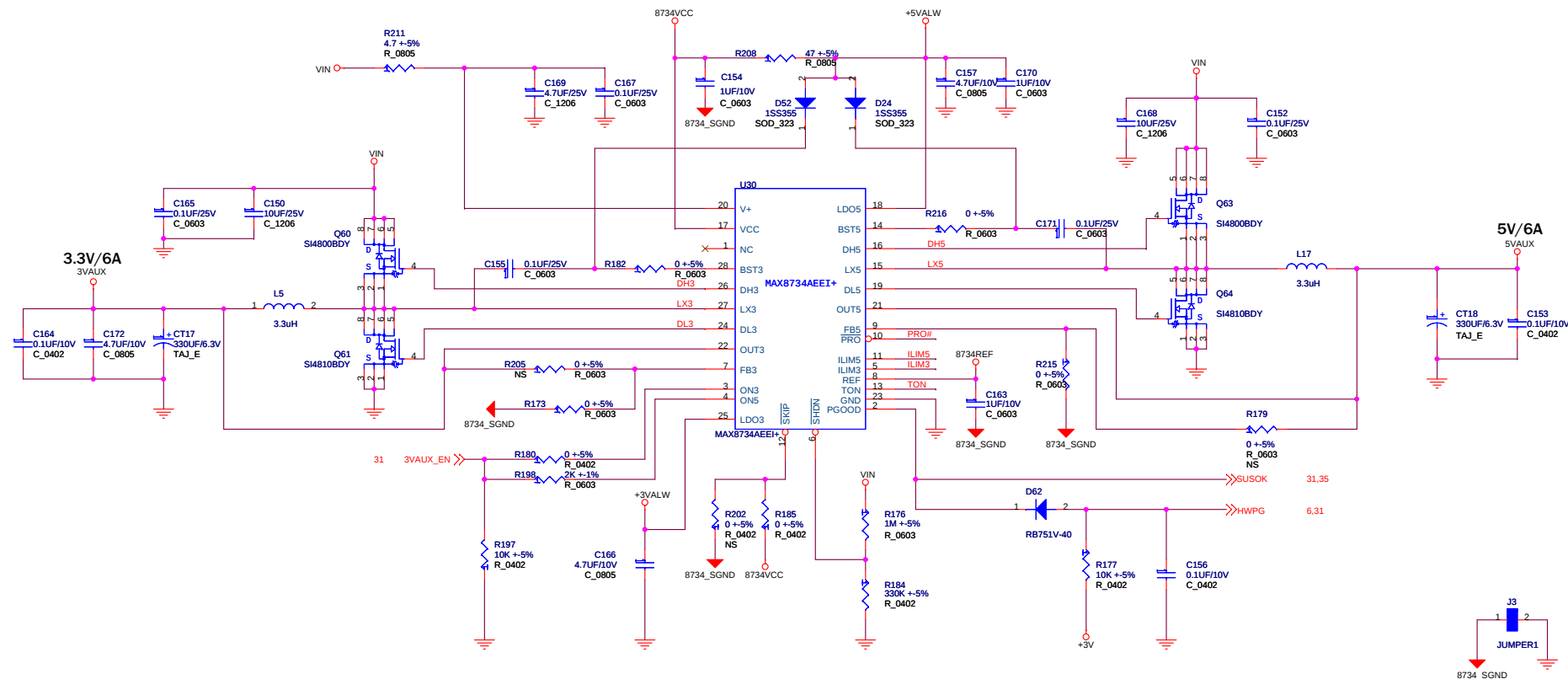
0.0710V
C_0402

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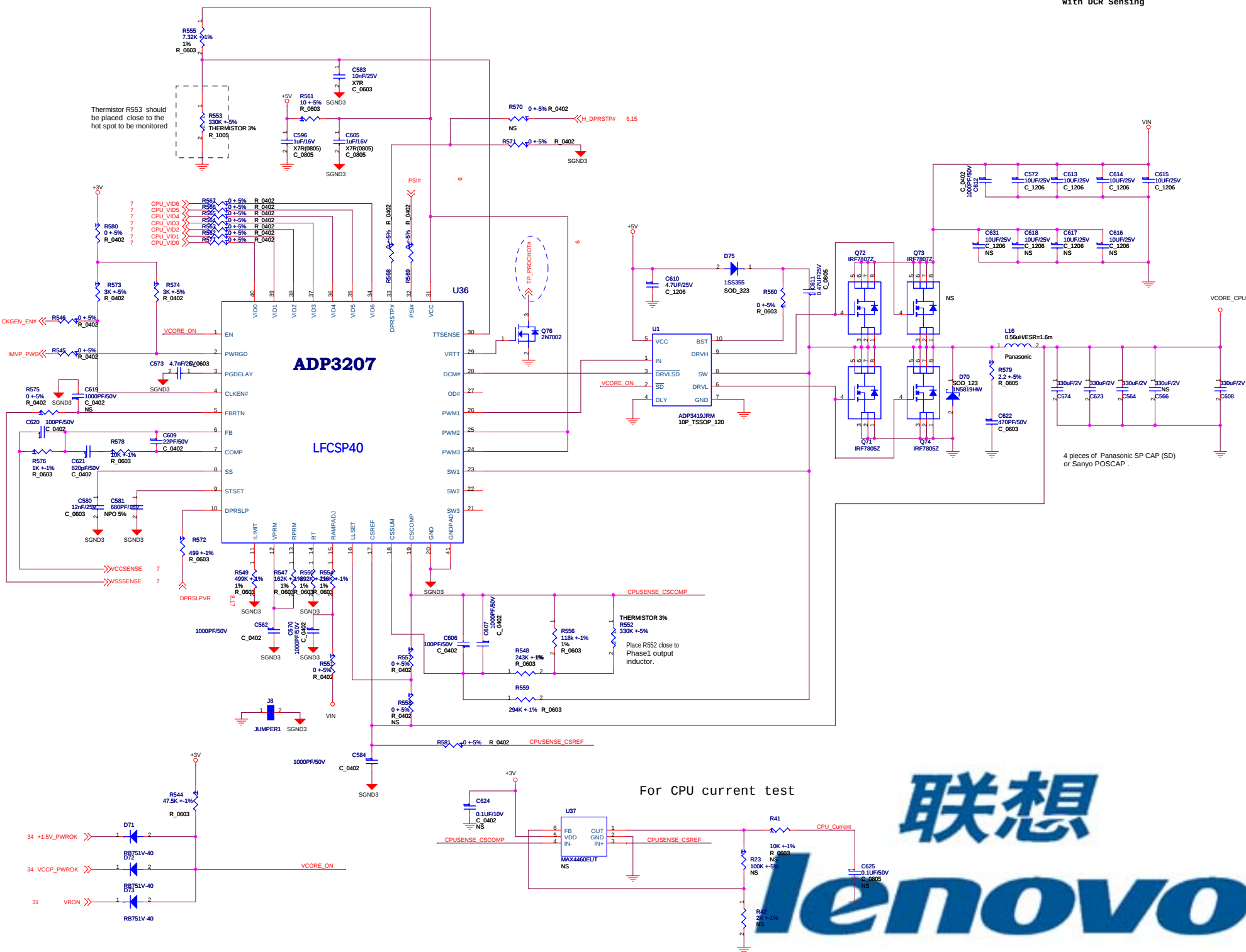


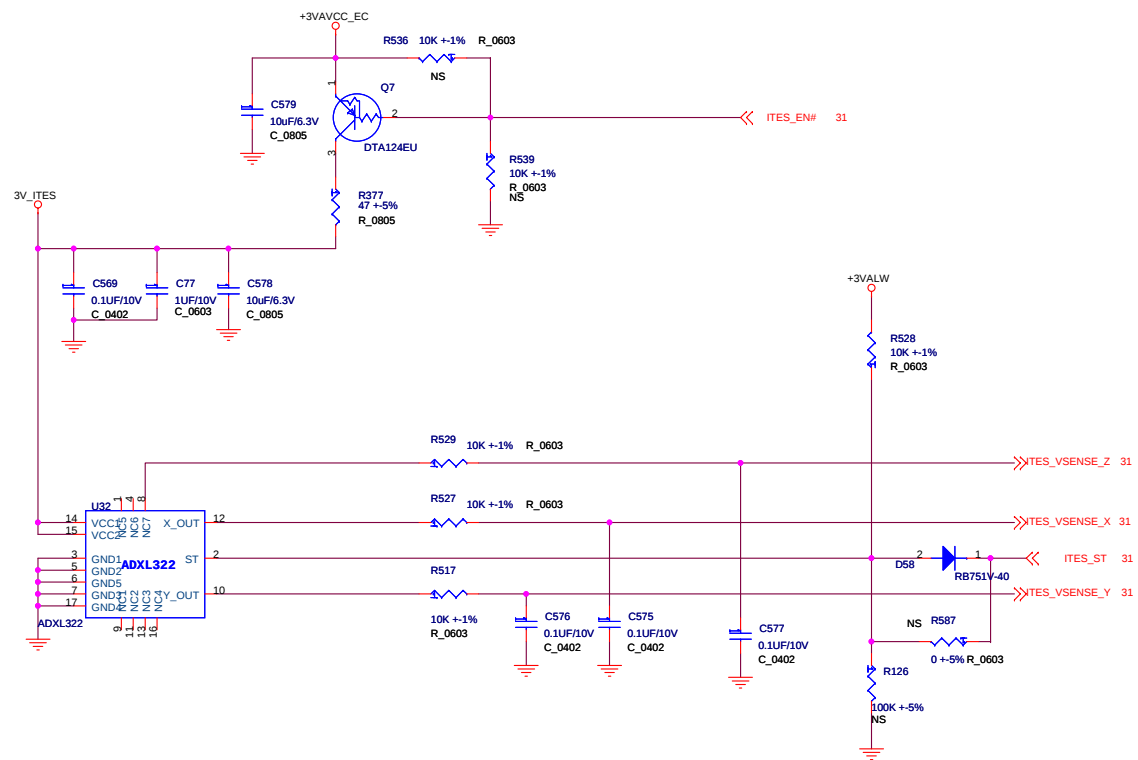


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